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U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE

SERIAL NO.

07/702,492

GROUP/ART UNIT

1304

ATTACHMENT  
TO  
PAPER  
NUMBER

6

NOTICE OF REFERENCES CITED

APPLICANT(S)

Inushina et al.

U.S. PATENT DOCUMENTS

		DOCUMENT NO.	DATE	NAME	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE
A		4719123	1-12-88	Haken et al.	427	53.1	—
B							
C							
D							
E							
F							
G							
H							
I							
J							
K							

FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUB-CLASS	PERTINENT SHTS. DWG.	PP. SPEC.
L									
M									
N									
O									
P									
Q									

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

R		"VLSI Fabrication Principles"; - Ghembi; ©1983; John Wiley & Sons; NY, NY; pp 422-424							
S									
T									
U									

EXAMINER

George Gendreau

DATE

4-92

\* A copy of this reference is not being furnished with this office action.  
(See Manual of Patent Examining Procedure, section 707.05 (a).)